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Application Number:	10598961		
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Title of Invention:	METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE		
rst Named Inventor/Applicant Name:	Tomoyuki Watanabe		
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)

		Total Files Size (in bytes)	1	18972				
Information	1							
Warnings:								
2	ree worksheet (FIO-673)	ree illio.pui	603fff82aa3cf80606433dec2d81eca412e59 d57		2			
2	Fee Worksheet (PTO-875)	fee-info.pdf	34017	no	2			
Information:								
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<u> </u>	issue ree rayment (r 10 osb)	Fee_Transmittal.pdf	6246 aa 16530d 9966978abde672181c3e27f 4bf35					
1	Issue Fee Payment (PTO-85B)	09-11-25_Form_PTOL 85_Issue_	84955	no	1			

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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